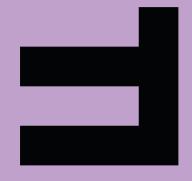
Smart Engineering Technologies (SET)

- Dynamic Research Collaboration: Our research group operates across departmental boundaries, fostering collaboration within FHV, RUN-EU, and with industry partners.
- Regional Expertise for the Tech Industry: We provide specialized knowledge and support to strengthen the technological capabilities of the regional industry.
- Support for Start-Ups and R&D Teams: We offer experimental development and research services tailored for start-ups, product developers, and research and development teams.

We are the youngest R&D group at FHV



f.l.t.r: Stefan Bonerz, Florian Ronacher, Horatiu Pilsan, Benedikt Terzer, Andre' Mitterbacher, Emanuel Matt, Christian Anselmi, Thomas Felder, Michael Ackers – SET Team



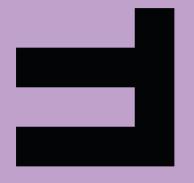
Our Focus

- Electronics
 - Mixed Signal Chip Design (Europractice Member)
 - Embedded Digital Design (Open-RISC-V)
 - FPGA-Design & Synthesis
- Smart Sensors
 - Deep edge sensor technology
 - Efficient designs
- Material Sciences
 - Advanced metall material
 - 3D Metal printing technologies

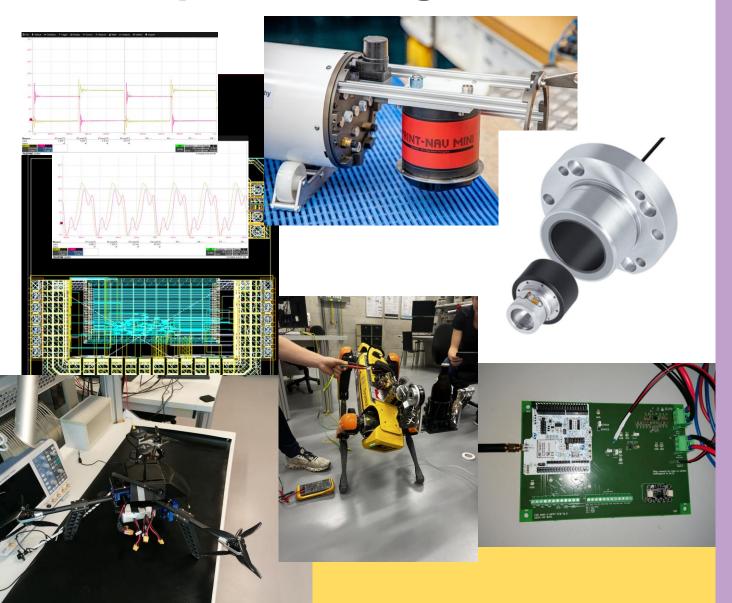
We are working for our industry - mainly applied R&D projects are in our focus



Christian Anselmi - Head of SET

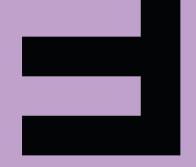


Development Insights



Service we can offer

- Research Services: We undertake specific research topics or selected aspects as required. Project fees will be agreed upon in advance.
- Master's and Bachelor's Theses: We supervise academic theses both inhouse and on your premises. A supervision fee will be applied accordingly.
- Access to Our Laboratories: Our laboratories and facilities are available for your use, either independently or with our assistance. Additionally, we organize tailored training sessions to suit your needs.



Contact



Prof. (FH) Dr. Stefan Bonerz Stefan.Bonerz@fhv.at

SET Member & Senior Lecturer for Electronics

Topics: Chipdesign, FPGA, Sensors, Mechatronical Systems



<u>Dipl.-Ing. Christian Anselmi</u> <u>Christian.Anselmi@fhv.at</u> <u>Set@fhv.at</u>

Head of SET & Senior Lecturer for Electronics

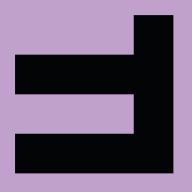
Topics:, IoT, FPGA & Embedded Systems, Advanced System Design



Dipl.-Ing. Dr. André MITTERBACHER
Andre.Mitterbacher@fhv.at

SET Member & Senior Lecturer for Electronics

Topics: Chipdesign, Analog- & Digitaldesign, FPGA & Embedded Systems



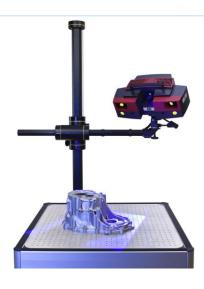
Technical Labs - tLabs











What we offer:

- Robotics, control and regulation technology
- Measurement and digitalization of free-form surfaces
- Test series on machine tools
- Tensile and compression testing, hardness testing
- Material analysis using spark spectrometers
- 3D coordinate measuring technology
- Machine tool diagnostics
- Production of prototypes
- Electrical and electronic measurement technology

Our services

Thanks to rapid progress in IT, it is increasingly possible to mathematically model and simulate the properties of materials. In many areas, however, we are still dependent on experiments. The laboratories of the Faculty of Engineering are equipped with the latest technologies, enabling a wide range of services and investigations to be offered.

Our students can use the technical laboratories 24/7

